

## WEST Search History

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DATE: Sunday, March 19, 2006

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	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L39	l38 and decod\$3	6
<input type="checkbox"/>	L38	java and L37	8
<input type="checkbox"/>	L37	(rockwell collins).as.	345
<input type="checkbox"/>	L36	rockwell.as and l30	0
<input type="checkbox"/>	L35	rockwell.as and l32	0
<input type="checkbox"/>	L34	rockwell.as and l2	0
<input type="checkbox"/>	L33	sun.as. and L32	7
<input type="checkbox"/>	L32	java processor	148
<input type="checkbox"/>	L31	L30 not l28	9
<input type="checkbox"/>	L30	(java with (isa or instruction set architecture))	94
<input type="checkbox"/>	L29	sun.as. and (java same (isa or instruction set architecture))	3
<input type="checkbox"/>	L28	java with isa	85
<input type="checkbox"/>	L27	plx same compil\$3	16
<input type="checkbox"/>	L26	plx86	0
<input type="checkbox"/>	L25	plx language	3
<input type="checkbox"/>	L24	plx language source code	1
<input type="checkbox"/>	L23	compil\$3 with source code with l14	7
<input type="checkbox"/>	L22	transmeta.as. and L21	0
<input type="checkbox"/>	L21	L19 with instruction\$1	79
<input type="checkbox"/>	L20	risa and L19	0
<input type="checkbox"/>	L19	arm with thumb	3386
<input type="checkbox"/>	L18	5438668.pn.	1
<input type="checkbox"/>	L17	L16 with program	119
<input type="checkbox"/>	L16	L13 with L14	779
<input type="checkbox"/>	L15	L13 with L14 with "same program"	0
	<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L14	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro-program\$1 or microprogram\$1	17324
<input type="checkbox"/>	L13	macroinstruction\$1 or macro-instruction\$1 or macrocode or macro-code or macro-operation\$1 or macrooperation\$1 or macro-op\$1 or macroop\$1 or	1582

macro-program\$1 or macroprogram\$1 or cisc instruction

*DB=PGPB,USPT; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L12	compil\$3 with L8 with L9	18
<input type="checkbox"/>	L11	compil\$3 same L10	0
<input type="checkbox"/>	L10	source code with L8 with L9	21

*DB=USPT,PGPB; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L9	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro- program\$1 or microprogram\$1	17324
<input type="checkbox"/>	L8	macroinstruction\$1 or macro-instruction\$1 or macrocode or macro-code or macro-operation\$1 or macrooperation\$1 or macro-op\$1 or macroop\$1 or macro-program\$1 or macroprogram\$1 or cisc instruction	1582

*DB=PGPB,USPT; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L7	vliw same arm same thumb	1
<input type="checkbox"/>	L6	compil\$3 with java byte code\$1	272
<input type="checkbox"/>	L5	L4 not L3	6
<input type="checkbox"/>	L4	compil\$3 same arm same java	11
<input type="checkbox"/>	L3	java same L1	5
<input type="checkbox"/>	L2	java sameL1	0
<input type="checkbox"/>	L1	compil\$3 with arm	143

END OF SEARCH HISTORY